

C3  
Page 16, line 22, after "by" insert --the output voltage--, same line 22 after  $V_L$  insert --via feedback of the output voltage  $V_L$  through MOS transistors  $Q_1...Q_i...$ , and  $Q_n$  to the gate of MOS transistor  $Q_i$ --.

IN THE CLAIMS:

Please amend the claims as follows:

7. (amended) A semiconductor integrated circuit comprising:

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a first circuit supplied with an external supply voltage, [which] said first circuit outputs a voltage whose amplitude has a first [small] dependence on an amplitude of the external supply voltage, when the external supply voltage is smaller than a predetermined voltage and a second dependence which is smaller than the first dependence, when the external supply voltage is larger than the [a] predetermined voltage;

an internal circuit supplied with the output voltage of said first circuit; and

a second circuit for use in performing an aging test of said internal circuit, and being supplied with the external supply voltage, [which] said second circuit outputs a voltage whose amplitude has a third [large] dependence, which is larger than the second dependence, on an amplitude of the external supply voltage; [and

an internal circuit supplied with the output voltage of said first circuit;]